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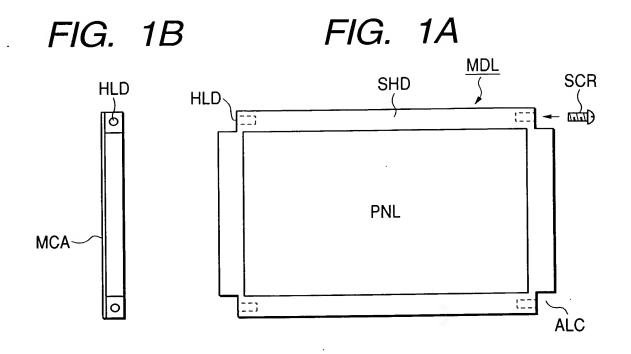
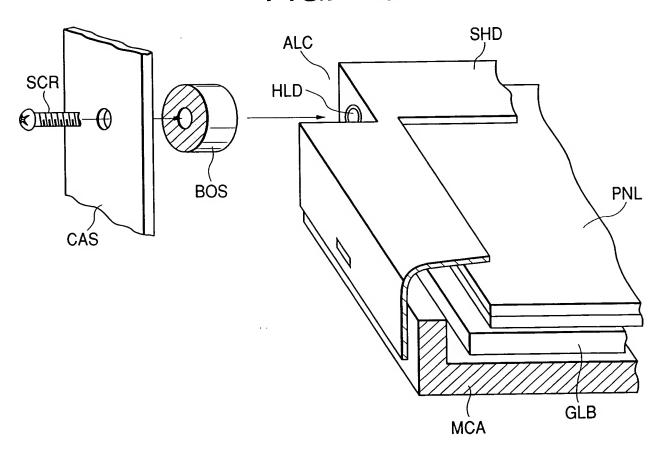


FIG. 1C



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FIG. 2A

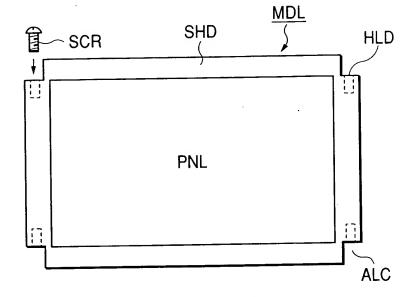
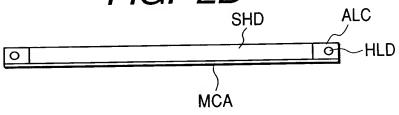
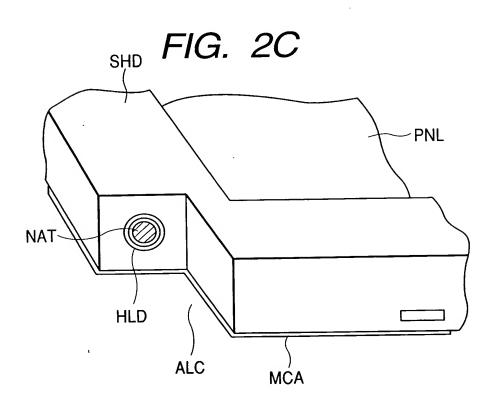


FIG. 2B





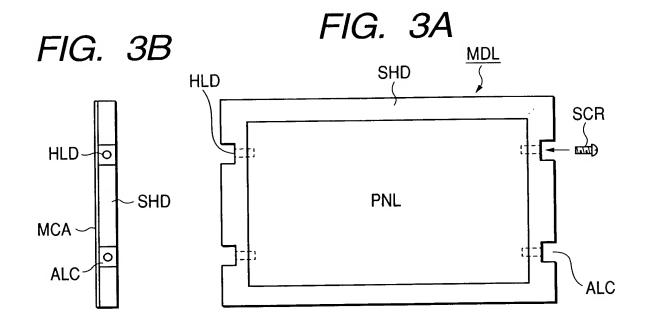


FIG. 3C

PNL
SHD
NAT
HLD
NAT

FIG. 4A

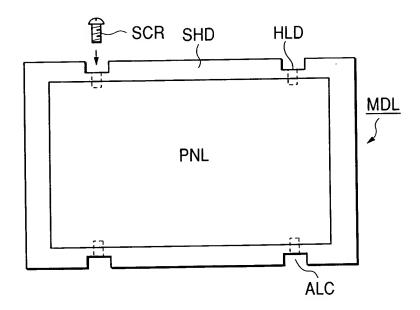
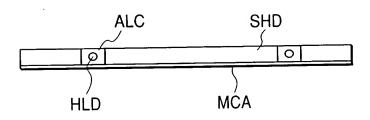


FIG. 4B



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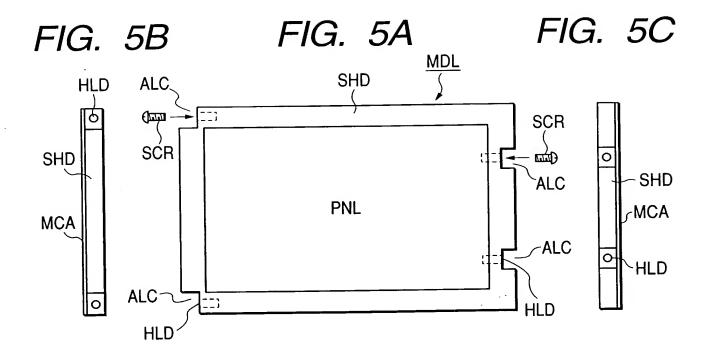
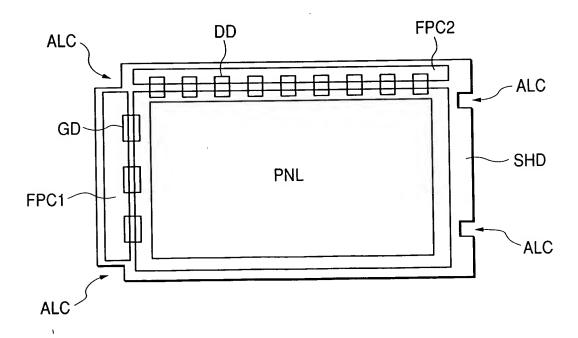


FIG. 6



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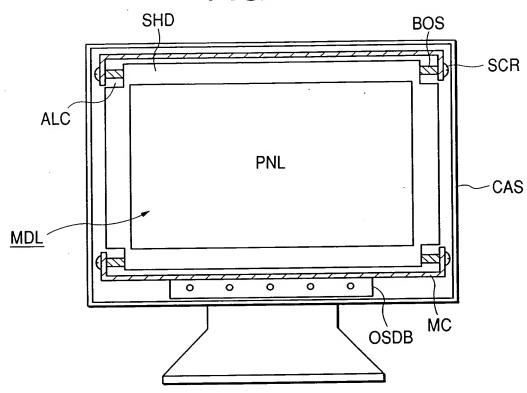


FIG. 7B

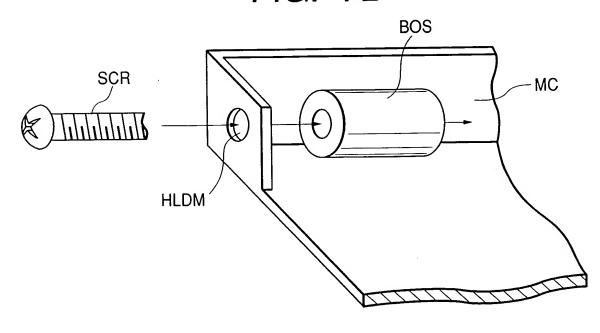


FIG. 8

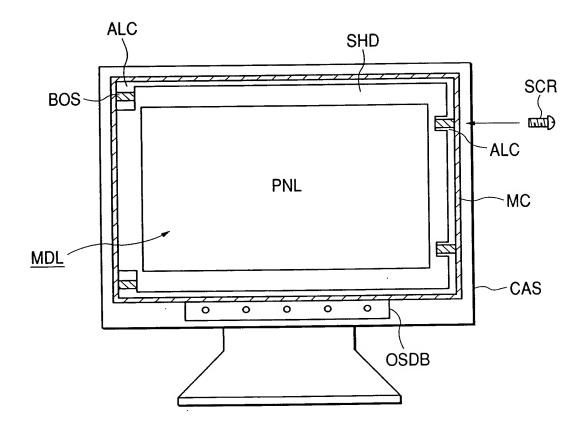


FIG. 9

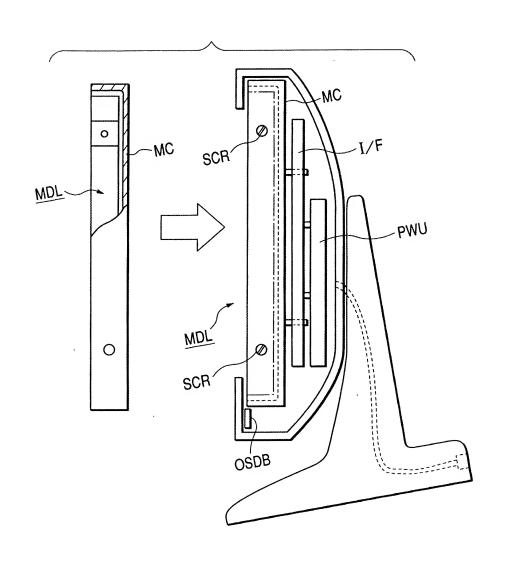


FIG. 10

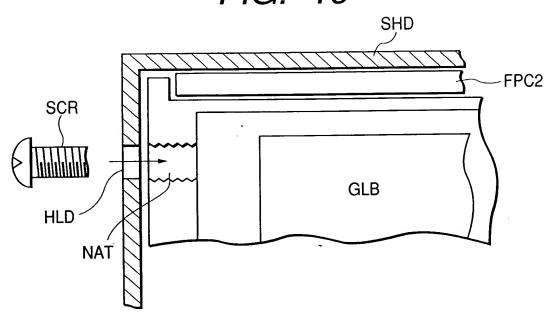


FIG. 11

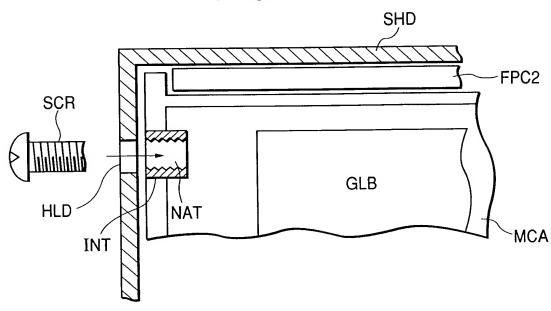


FIG. 12

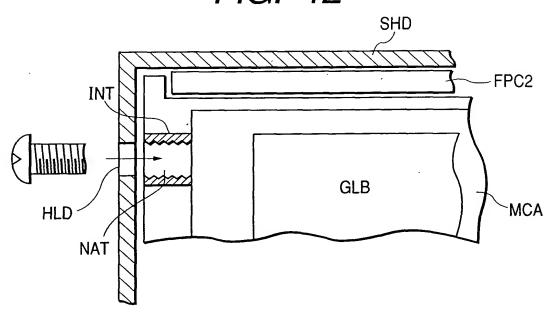


FIG. 13

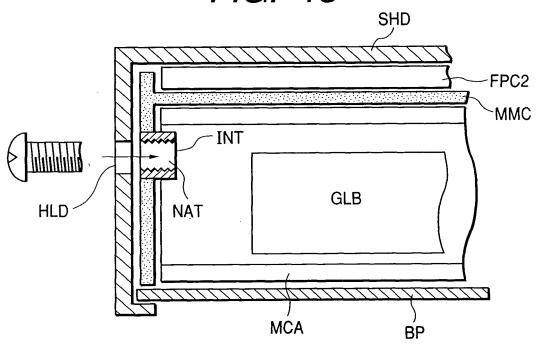


FIG. 14

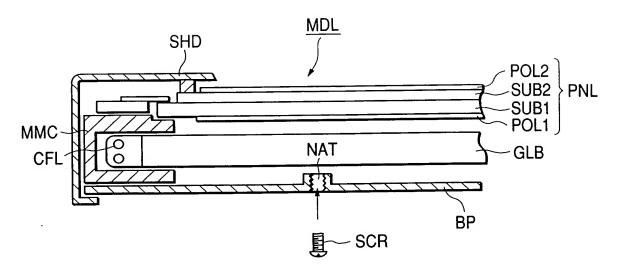


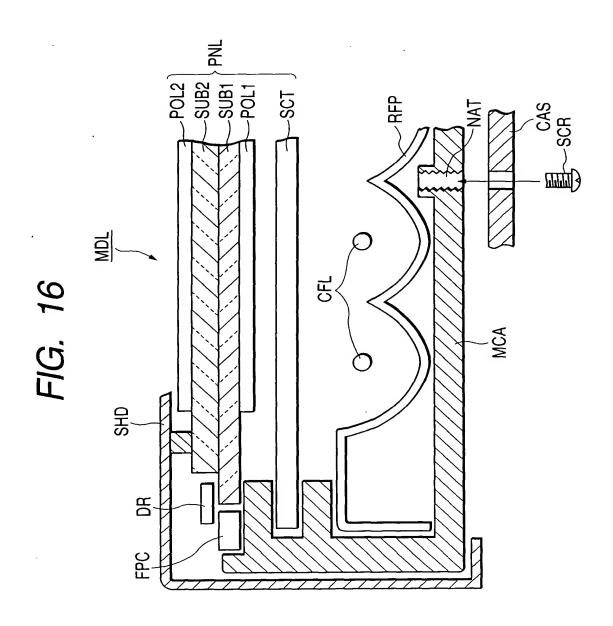
FIG. 15

NAT

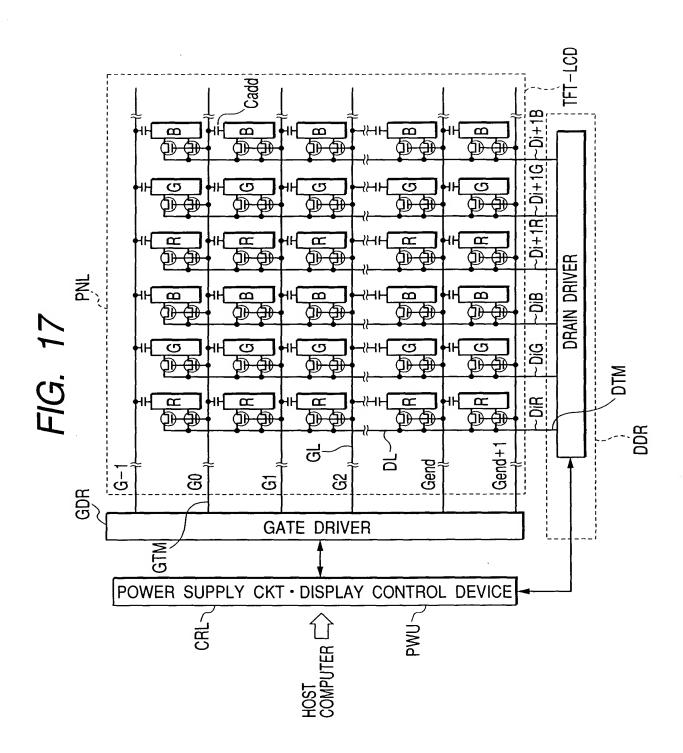
I/F PWU

BP

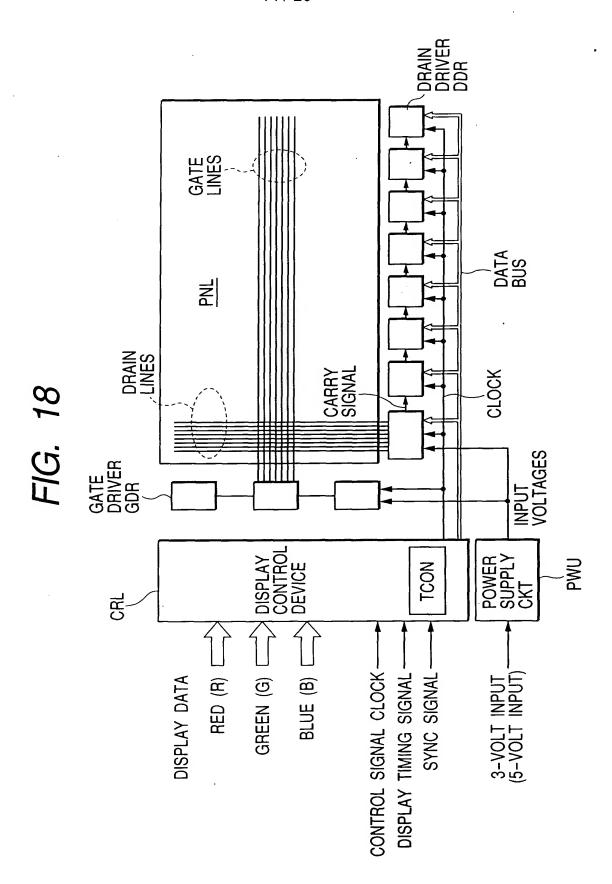
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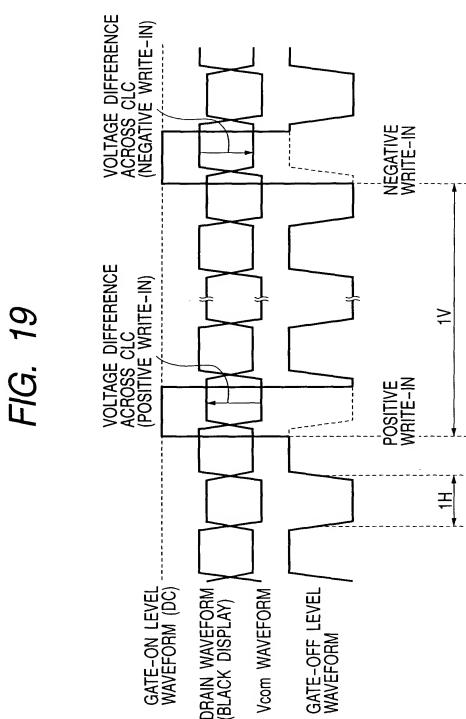
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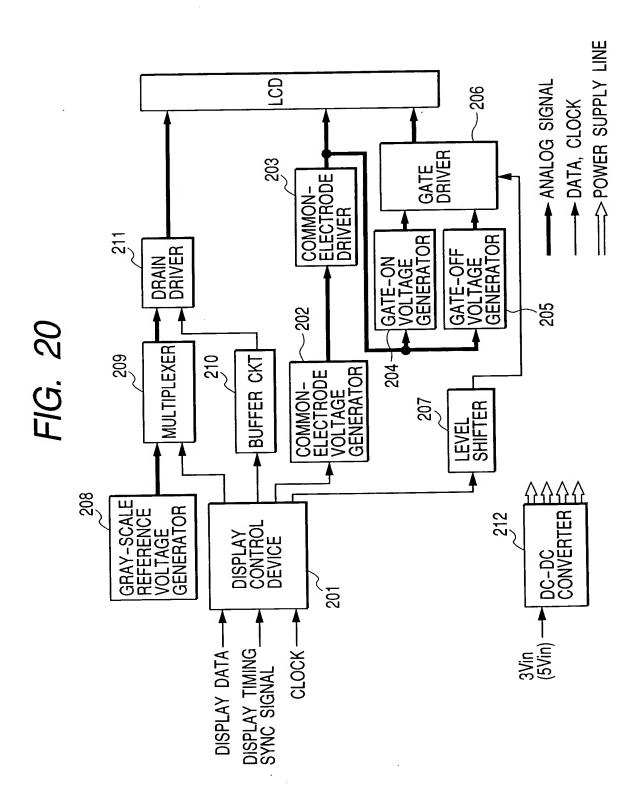
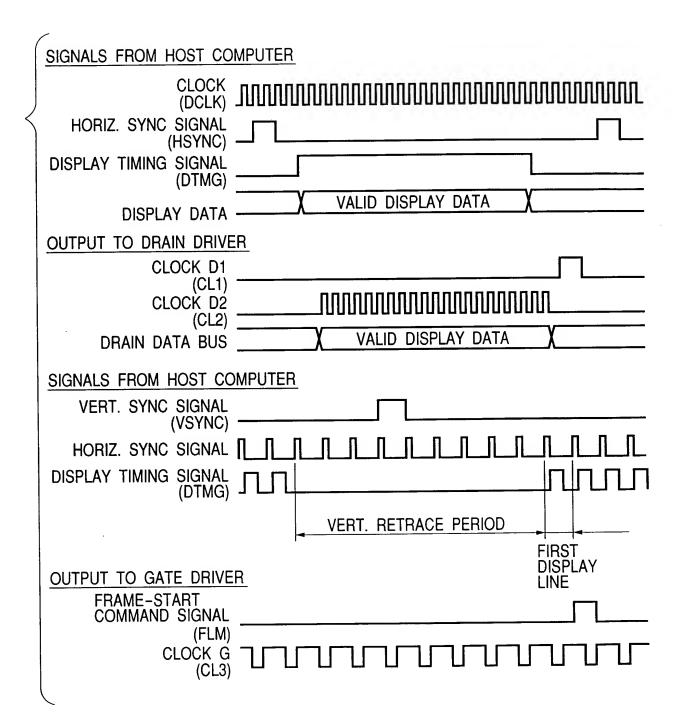


FIG. 21



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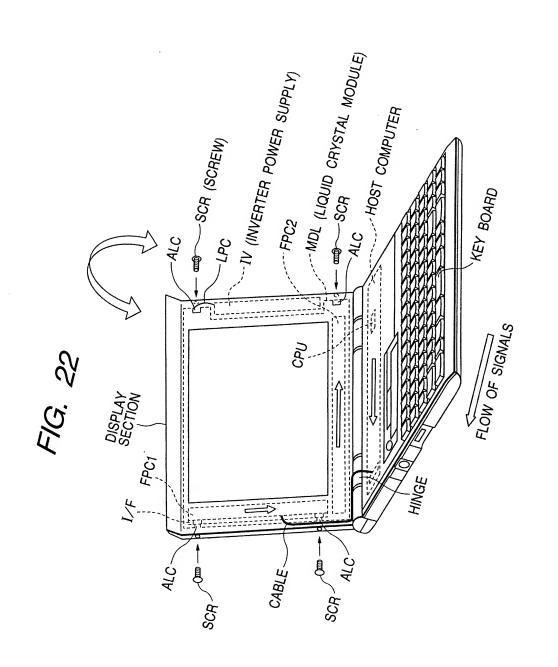
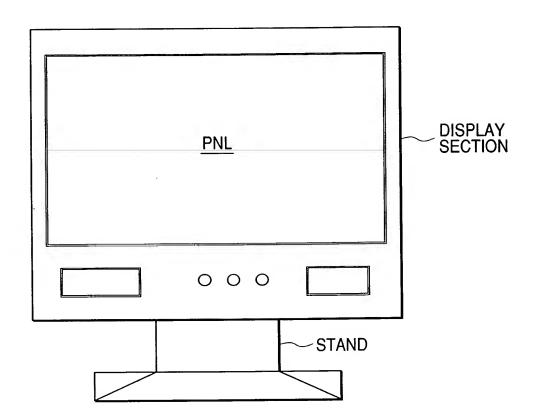
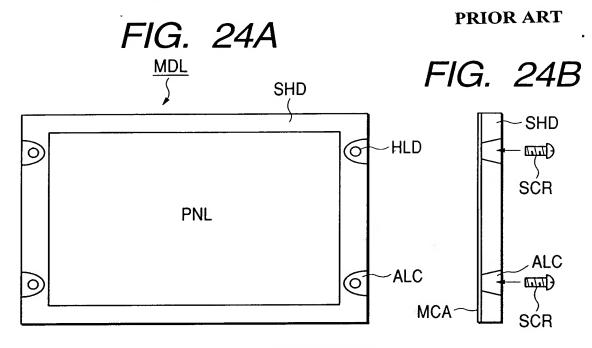


FIG. 23



PRIOR ART 20 / 20



PRIOR ART

FIG. 24C

